

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8	lsi same terminals same (greater more larger less) same (number plurality) same (chips modules arrays) and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/27 13:04
S4	20	controller same chips same interface same output\$3 same terminals	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 10:21
S5	37	controller.ti. and interface same output\$3 same terminals and chips	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/20 16:42
S6	19	controller.ti. and (various different plurality types).ti. and interface same output\$3 and terminal\$1	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/20 16:47
S7	36	controller.ti. and (various varied differ\$3 types kinds).ti. and interfac\$3 same output\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/20 16:51
S8	26	controller.ti. and (various varied differ\$3 types kinds).ti. and 7\$2/\$3.ccls. and interfac\$3 same output\$3	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/20 16:54
S9	57	controller.ti. and (various varied differ\$3 types kinds).ti. and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/20 16:54
S10	13	controller same interface same output\$3 same input\$3 same terminals same different same types	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:08
S11	149	controller same (chips memories arrays) same interface same terminals	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/25 11:59
S12	138	controller same (chips memories) same interface same terminals	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:16
S13	8	controller same (chips memories) same interface same terminals and controller.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/25 12:00
S14	39	controller same (chips memories) same interface same terminals and controller.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:45
S15	1	("20010054135").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 10:21
S17	4	controller same interface same output\$3 same input\$3 same terminals same "different types"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:09

S19	48	controller.ab. and interface same output\$3 same input\$3 same terminals same (memories chips kinds types)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:12
S21	2	(controller control).ti. and interface same terminals same (memories chips) same (types kinds)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:13
S22	9	(controller control).ti. and interface same terminals same (memories chips modules arrays) same (types kinds)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:15
S23	29	(controller control).ti. and interface same terminals same "different types"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:15
S24	64	controller same (chips memories) same interface same terminals and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:39
S25	21	controller same chips same interface same terminals and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:41
S26	5	controller.ab. and (chips memories).ab. and (different various types kinds).ab. and interface same terminals and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:41
S27	119	interface same terminals same "chip select"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:46
S28	8	interface same terminals same "chip select" and controller.ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:46
S29	26	interface same terminals same "chip select" and controller.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:48
S30	205	interface same terminals and "chip select" and (controller microcontroller).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:49
S31	136	interface same terminals and "chip select" and (control controller microcontroller).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:50
S32	2	interface same terminals and "chip select" and (control controller microcontroller).ti. and (different various).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 11:50
S33	19	interface same terminals same "chip select" and (control controller microcontroller).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 12:25

S34	7	interface same terminals and "chip select" and lsi and "different types" and (control controller microcontroller).ti.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 12:34
S35	19	interface same terminals and "chip select" and lsi and "different types" and (controller microcontroller).ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 12:36
S36	9	interface same controller same terminals and "chip select" and lsi and "different types"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 12:45
S37	60	interface and terminals and "chip select" and lsi and ("different types" "different kinds") and programmable	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 13:00
S38	108	interface and terminals and "chip select" and ("direct memory access" "direct access") and lsi and programmable	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 13:02
S39	194	interface and terminals and "chip select" and ("direct memory access" "direct access") and lsi	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 13:03
S40	55	interface and terminals and "chip select" and ("direct memory access" "direct access") and lsi and controller.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 13:17
S41	3	interface and (terminals same ("different types" "different kinds" various)) and "chip select" and ("direct memory access" DMA) and lsi and controller.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 13:18
S42	15	interface and (terminals same ("different types" "different kinds" various)) and "chip select" and ("direct memory access" DMA) and lsi	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 13:27
S43	15	interface and (terminals same ("different types" "different kinds" various)) and "chip select" and ("direct memory access" DMA) and lsi and programmable	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 14:47
S44	4	programmable same "memory unit" same specification	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 14:48
S45	119	programmable same "memory unit" and 7\$2/\$3.ccls. and controller.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 15:41
S47	162	terminals same data same address same clock and 7\$2/\$3.ccls. and controller.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 15:45

S48	1	"data terminal" same "address terminal" same "clock terminal" and 7\$2/\$3.ccls. and controller.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 15:46
S49	24	"data terminal" same "address terminal" same "clock terminal"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 16:00
S54	70	number same terminals same chips same controller	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 16:02
S55	6	number same terminals same chips same (larger greater) same controller	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 16:06
S56	282	number same terminals same chips same (larger greater)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 16:21
S57	94	terminals same chips same (larger greater) near2 number	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 16:22
S58	19	chips same terminals near2 (larger greater) near2 number	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 16:25
S59	46	(chips modules arrays memories) same terminals near3 (larger greater) near3 number	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 16:55
S60	11	(number near3 (chips modules arrays memories)) same (terminals near3 (larger greater) near3 number)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:07
S61	164	specification near3 chip same (type command address latency)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 16:58
S62	4	specification near3 "memory chip" same (type command address latency)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 16:59
S65	36	specification near3 "memory chip"	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:00
S66	43	specification near3 chip same controller	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:03
S68	162	specification near3 chip same (type command sequence address)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:04
S69	10	specification near3 chip same (type command sequence address) and controller.ab.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:04
S74	3	number same terminals same larger same chips same controller	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:11

S75	17	number same terminals same (larger greater more) same chips same controller	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:16
S76	15	number same terminals same (larger "greater than" more less "smaller than") same chips same controller	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:19
S78	48	"more terminals" same chips	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:21
S80	1202	"more terminals" and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:22
S82	3	"more terminals" same chips and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:23
S86	95	number same terminals same chip\$1 same controller and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:28
S87	7	terminals same interface same different same chips and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/26 17:31
S88	181	terminal\$1 same interface same chips and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/27 10:53
S90	171	terminal\$1 same (greater more larger) same (number plurality) same (chips modules arrays memories) same interface and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/27 10:55
S91	46	terminal\$1 same (greater more larger) same (number plurality) same (chips modules arrays memories) same interface same controller and 7\$2/\$3.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/01/27 13:03